M

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**PATENT** 

# DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

the specification of which: (check one)

My residence, post office address and citizenship are as stated below next to my name;

I believe I am an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

### DELAY CORRELATION ANALYSIS AND REPRESENTATION FOR VITAL COMPLIANT VHDL MODELS

XXX	is attached hereto.			
	as Application Serial	ket Number No	_	
		wed and understand the ny amendment referred		ove identified specification, including
	owledge the duty to di ance with 37 CFR 1.56		ch is material to th	ne patentability of this application in
invento	r's certificate listed bel		ied below any forei	y foreign application(s) for patent or ign application for patent or inventor's of which is claimed:
Prior F	oreign Application(s):			Priority Claimed
(Numb	er) (C	ountry)	(Filing Date)	Yes <u>X</u> No
I hereby claim the benefit of United States priority under 35 USC 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in a listed prior United States application in the manner provided by the first paragraph of 35 USC 112, I acknowledge the duty to disclose information material to the patentability of this application as defined in 37 CFR 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:				
(Application	ation Serial #)	(Filing Date)		(Status)
I hereb	y declare that all staten	nents made herein of my	y own knowledge a	are true and that all statements made

patent issued thereon.

on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 USC 1001 and that such willful false statements may jeopardize the validity of the application or any



POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Lynn L. Augspurger	Reg. No. 24,227	Martin Fleit	Reg. No. 16,900
Floyd A. Gonzalez	Reg. No. 26,732	Robert C. Kain	Reg. No. 30,648
Christopher A. Hughes	Reg. No. 26,914	Jon A. Gibbons	Reg. No. 37,333
Stanley B. Green	Reg. No. 24,351	Jose Gutman	Reg. No. 35,171
´John E. Hoel	Reg. No. 26,279	Stephen Bongini	Reg. No. 40,917
Lawrence D. Cutter	Reg. No. 28,501	William R. Trueba, Jr.	Reg. No. 48,002
William A. Kinnaman, Jr.	Reg. No. 27,650	Jeffrey Giunta	Reg. No. 42,583
William B. Porter	Reg. No. 33,135	Mark Terry	Reg. No. 47,133
Andrew J. Wojnicki, Jr.	Reg. No. 43,995	·	
Joseph C. Redmond, Jr.	Reg. No. 18,753		

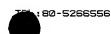
Direct all telephone calls to Jose Gutman (561) 989-9811 and all correspondence to Customer Number 23334



FULL NAME OF INVENTOR: Marvin J. RICH			
INVENTOR'S SIGNATURE: Marin J. Rich DATE: 12/27/01			
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CITIZENSHIP: United States			
POST OFFICE ADDRESS: Same as above			
FULL NAME OF INVENTOR: Ashutosh MISRA			
INVENTOR'S SIGNATURE:DATE:			
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POST OFFICE ADDRESS: Same as above

CITIZENSHIP: India



#### EXPRESS MAIL LABEL NO. EL863783629US

**PATENT** 

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### DELAY CORRELATION ANALYSIS AND REPRESENTATION FOR VITAL COMPLIANT VHDL MODELS

the sp	ecification of which: (check o	one)		
xxx	is attached hereto.			
	was filed on under Attorney's Docket N as Application Serial No and was amended on	umber		
	y state that I have reviewed a ims, as amended by any an			bove Identified specification, including
	owledge the duty to disclos lance with 37 CFR 1.56.	e information whi	ch is material to t	he patentability of this application in
invent		id have also identif	ied below any fore	y foreign application(s) for patent o Ign application for patent or inventor's of which is claimed:
Prior F	oreign Application(s):			Priority Claimed
(Numb	er) (Countr	y)	(Filing Date)	Yes <u>X</u> No <sup>1</sup>
below prior U the dui	and, insofar as the subject r inited States application in the y to disclose information mai ed between the filing date of	natter of each of t ne manner provide terial to the patents	he claims of this a d by the first para bility of this applic	any United States application(s) listed application is not disclosed in a listed graph of 35 USC 112, I acknowledge ation as defined in 37 CFR 1.56 which or PCT international filing date of this
(Applic	cation Serial #)	(Filing Date)		(Status)
on info knowle under	ermation and bellef are belied by the belied by the belied by the believes are believed are believed by the believes are believed by the believes are believes are believed by the believes are believes are believed by the believes are belie	eved to be true; a ents and the like so	nd further that the made are punish	are true and that all statements made ese statements were made with the able by fine or imprisonment, or both the the validity of the application or an



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